

**REMARKS**

**Present Status of the Application**

Favorable reconsideration of this application, as presently amended, is respectfully requested. Claims 1-4 are now pending in this application. This application is a Continuation of patent application Serial No. 09/801,350, filed March 7, 2001. Examination of the application is respectfully requested.

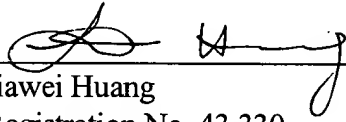
**CONCLUSION**

For at least the foregoing reasons, it is believed that all pending claims 1-4 are in proper condition for allowance. If the Examiner believes that a conference would be of value in expediting the prosecution of this application, he is cordially invited to telephone the undersigned counsel to arrange for such a conference.

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Respectfully submitted,  
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**Version with markings to show changes made**

**In The Claims:**

Claim 1 has been amended as follows:

1. (Amended) An electrostatic discharge (ESD) protection circuit, suitable for use on the I/O pad, the ESD protection circuit comprising:

a silicon controlled rectifier (SCR) circuit, which comprises a first connection terminal, a second connection terminal, and a third connection terminal, wherein the first connection terminal and the second connection terminal are respectively connected to the I/O pad and a ground voltage, so as to discharge the electrostatic charges; and

an anti-latch-up circuit, which comprises a fourth connection terminal, a fifth connection terminal, and a sixth connection terminal, respectively coupled to a voltage source, the ground voltage, and the third connection terminal of the SCR circuit, whereby an anti-latch-up signal is sent from the sixth connection terminal[, so that]to the SCR circuit[ is not unexpectedly, causing latch-up of the ESD protection circuit].

Claims 2-4 remained as ever.